

09/08/03
16638 U.S. PTO

UTILITY APPLICATION

Attorney Docket No.: ASMJP.134AUS

First Named Inventor: Naoto Tsuji

Title: INTERLAYER INSULATION FILM USED FOR MULTILAYER
INTERCONNECT OF SEMICONDUCTOR INTEGRATED
CIRCUIT AND METHOD OF MANUFACTURING THE SAME

Express Mail Label No.: EV 308022742 US

Direct all correspondence to Customer No.: 20995

Date: September 8, 2003

Page 1 of 2

Mail Stop Patent Application

United States Patent and Trademark Office

PO Box 1450

Alexandria, VA 22313-1450

22386 U.S. PTO
10/657416
09/08/03

The following enclosures are transmitted herewith to be filed in the patent application of:

Inventor(s):

1. Naoto Tsuji
2. Fumitoshi Ozaki
3. Satoshi Takahashi

Assignee of this application: ASM JAPAN K.K.

Address: 23-1, 6-chome, Nagayama, Tama-shi, Tokyo 206-0025 Japan

APPLICATION ELEMENTS:

(X) Specification in twenty-four (24) pages.

(X) Drawings in one (1) sheet.

(X) Declaration by Inventors in 2 pages.

- Originally signed

FOREIGN CONTINUITY INFORMATION:

| Country | Application No. | Filing Date | Priority Claimed |
|---------|-----------------|-------------|------------------|
| Japan | 2002-262304 | 09/09/02 | Yes |

OTHER APPLICATION PARTS:

(X) Recordation form cover sheet and Assignment in 3 total pages (cover sheet & assignment).

(X) Certified copy of priority application (Application No.: 2002-262304, Country: Japan).

(X) Power of attorney form and copy of assignment in 2 pages.

(X) Information Disclosure Statement and PTO-1449 in 2 total pages (IDS and 1449).

(X) Two (2) references

(X) Return prepaid postcard.

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FILING FEES:

| FEE CALCULATION | | | | |
|--------------------|-------------|--------------|----------------------|--------------|
| FEE TYPE | | FEE CODE | CALCULATION | TOTAL |
| Basic Utility | | 1001 (\$750) | | \$750 |
| Recordation Fee | | 8021 (\$40) | 1 x 40 = | \$40 |
| Excess Claims > 20 | 21 - 20 = 0 | 1202 (\$18) | 1 x 18 = | \$18 |
| Independent > 3 | 3 - 3 = 0 | 1201 (\$84) | 0 x 84 = | \$0 |
| | | | TOTAL FEE DUE | \$808 |

(X) A check in the amount of **\$808.00** to cover the Total Fee Due is enclosed.

The Commissioner is hereby authorized to charge any additional fees which may be required, now or in the future, or credit any overpayment to Account No. 11-1410.



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MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

Attorney Docket No. : ASMJP.134AUS

Applicant(s) : Naoto Tsuji, et al.

For : INTERLAYER INSULATION FILM USED
FOR MULTILAYER INTERCONNECT OF
SEMICONDUCTOR INTEGRATED CIRCUIT
AND METHOD OF MANUFACTURING THE
SAME

Attorney/Agent : Gordon H. Olson/Katsuhiko Arai

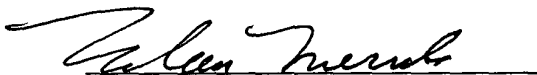
"Express Mail"
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Date of Deposit : September 8, 2003

I hereby certify that the accompanying

Transmittal letter; specification in 24 pages; 1 sheets of drawing; **SIGNED**
Declaration by Inventor in 2 pages; Recordation Form Cover Sheet and
Assignment in 3 pages; Power of Attorney by Assignee in 2 pages; Information
Disclosure Statement, PTO Form 1449 with 2 references; A certified copy of
Japanese Patent Application No. 2002-262304 filed 09/09/02; \$808.00 Check for
Filing Patent Application and Assignment Fee; Return Prepaid Postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Nelson Merida

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